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LSI Logic Corporation
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Milpitas, CA 95035

Facsimile Transmittal Letter

Date February 2, 2006Number of pages including cover sheet 3

To: US PTO, Mail Stop IDS

TC 2825

Fax No. **571-273-8300**

Phone No.

CC:

From: Connie del Castillo

Intellectual Property Paralegal

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REMARKS:

☐ Urgent☐ For your review☒ Reply ASAP☐ Please comment

Application Number: 10/694,208
Filing date: October 27, 2003
First named inventor: Alexandre Andreev et al.
Attorney docket number: 03-2064

Transmitted herewith for filing via facsimile:

- Information Disclosure Statement Letter
- Information Disclosure Statement by Applicant PTO/SB/O8A

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Connie del Castillo

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Alexandre Andreev et. al.

Serial No. : 10/694,208

Filed : **October 27, 2003**

For : Process and Apparatus for Placement of Cells in an IC during Floor Plan Creation

00: 00: 00

Group Art Unit : 2825

Examiner : Dinh, Paul

Atty Docket : / 03-2064

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I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

2/02/06 *Connie del Castillo*
 Date Signature

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation
1621 Barber Lane, MS D-106
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408-433-7475

Date: 2/1/17

Respectfully submitted,

W.R. Allen

Timothy Croll

Reg. No. 36,771

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PTO/SB/08A (10-96)

Approved for use through 10/31/99. OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				<i>Complete if Known</i>	
				Application Number	10/694,208
				Filing Date	October-27, 2003
				First Named Inventor	Alexandre Andreev
				Group Art Unit	2825
				Examiner Name	Paul Dinh
Sheet	1	of	1	Attorney Docket No.	/ 03-2064

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Number	Kind Code (if known)		
		6,725,432		Chang et al.	04-20-2004

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Examiner signature		Date considered	
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